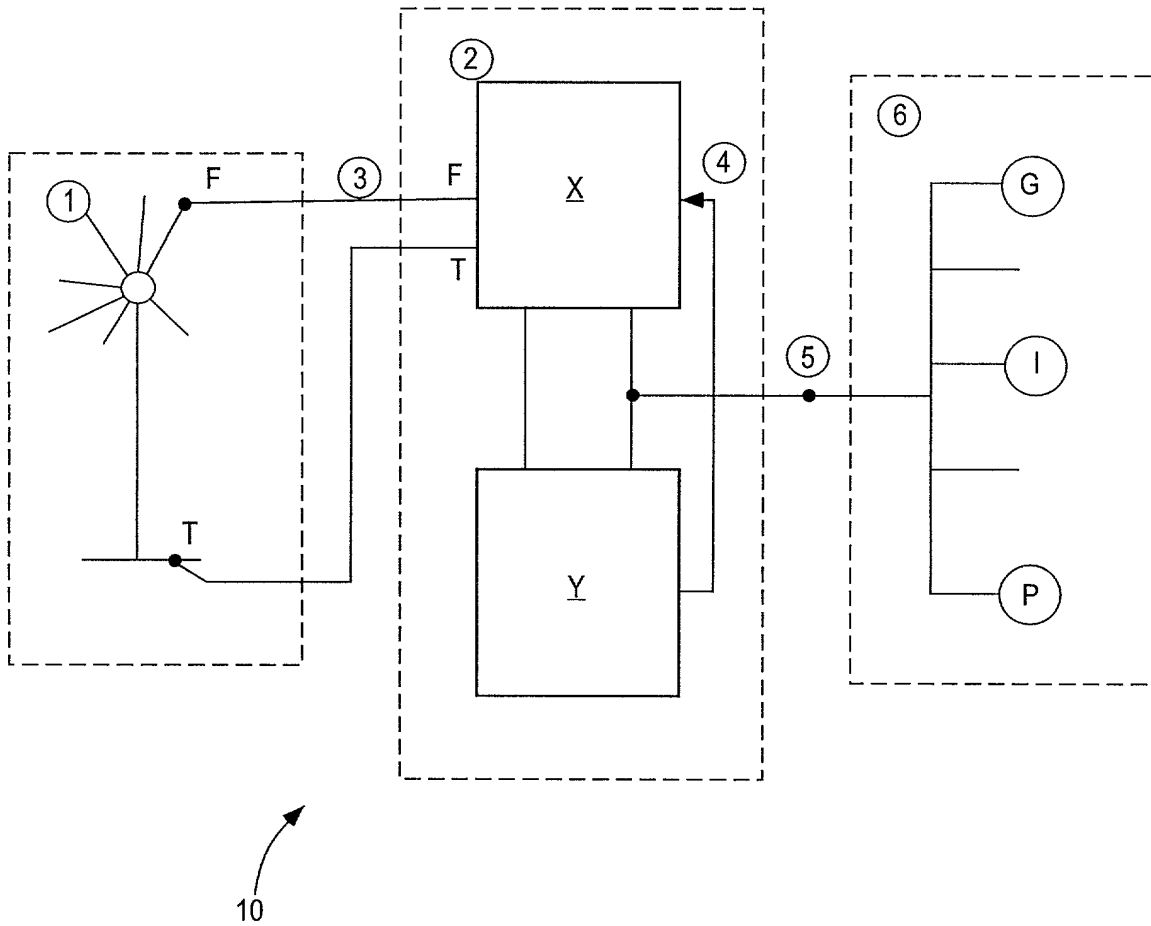


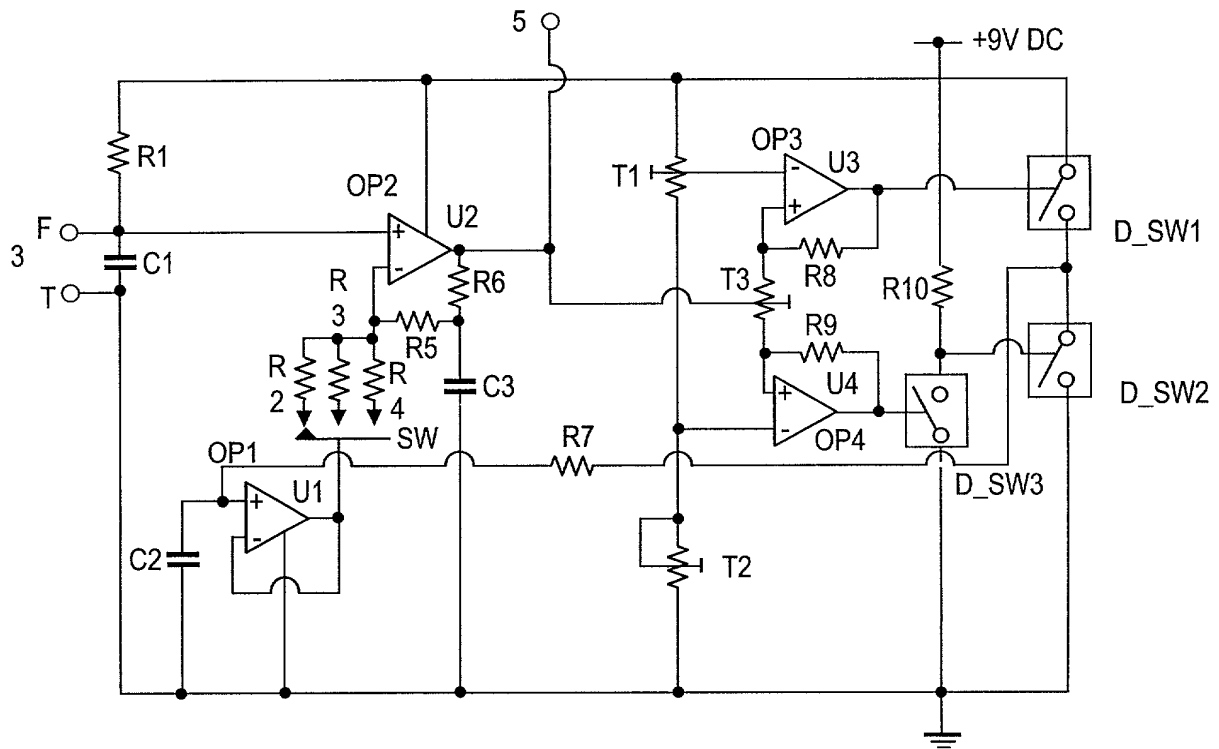
1/10

FIG. 1

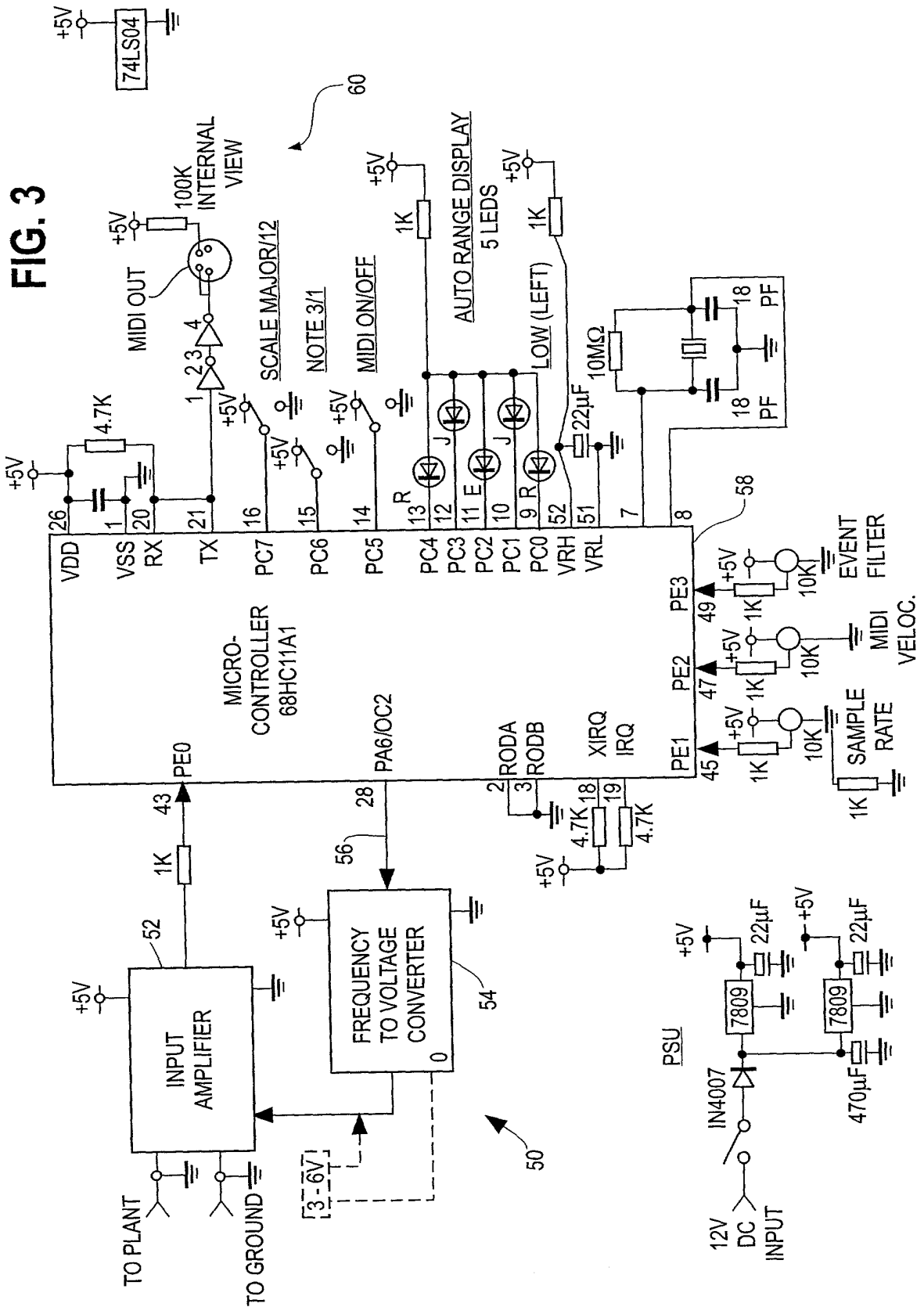


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**FIG. 2**



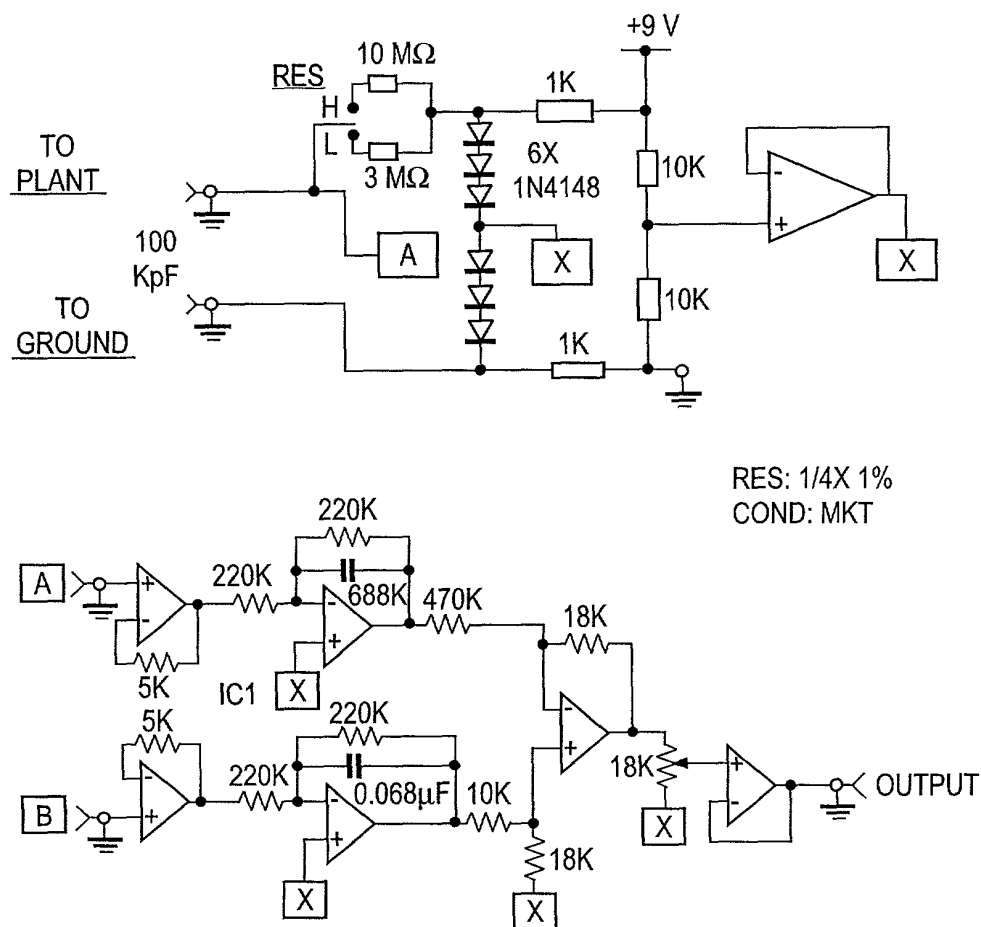
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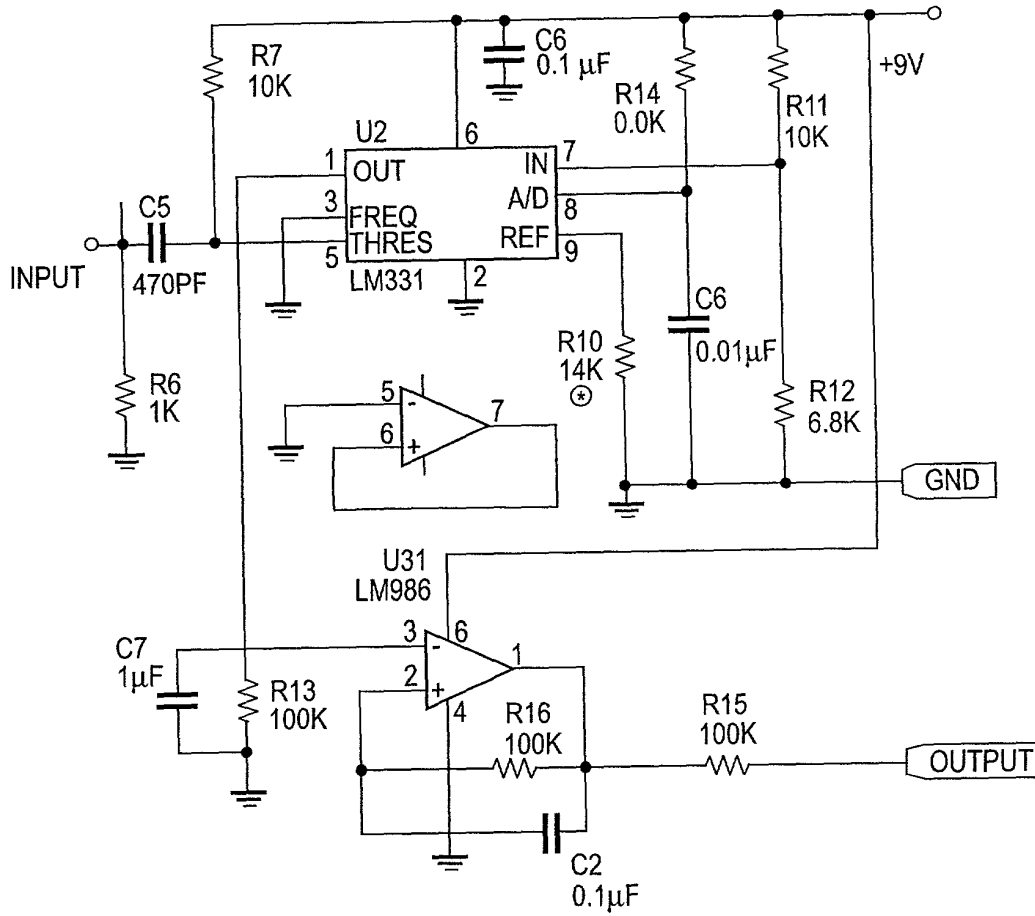
FIG. 4

INPUT AMPLIFIER

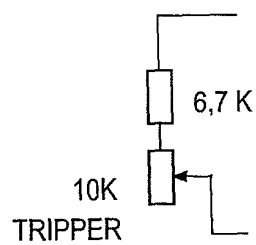


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FIG. 5



⊛ OR



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102

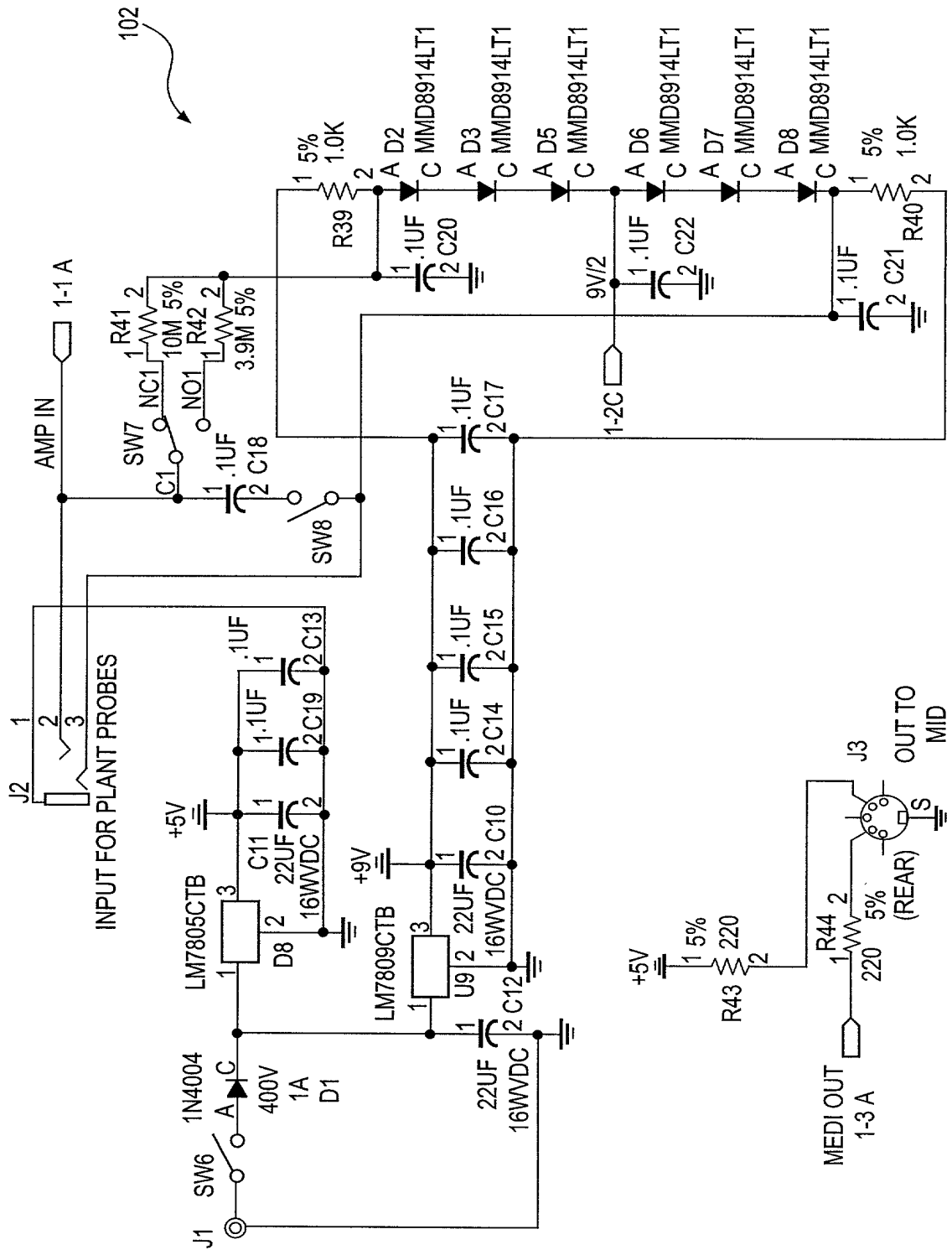


FIG. 7

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FIG. 7a	FIG. 7b	FIG. 7c
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**FIG. 7a**

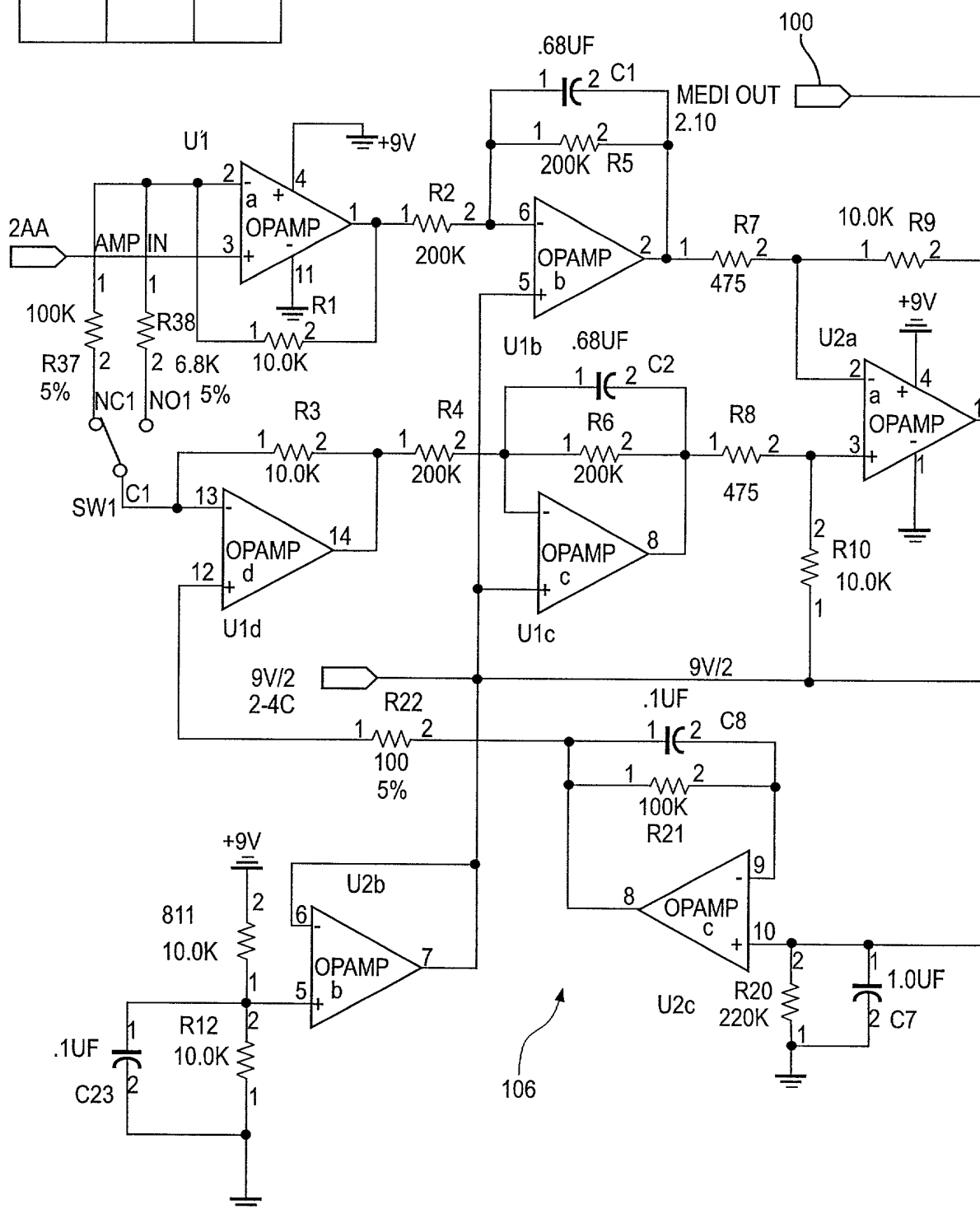
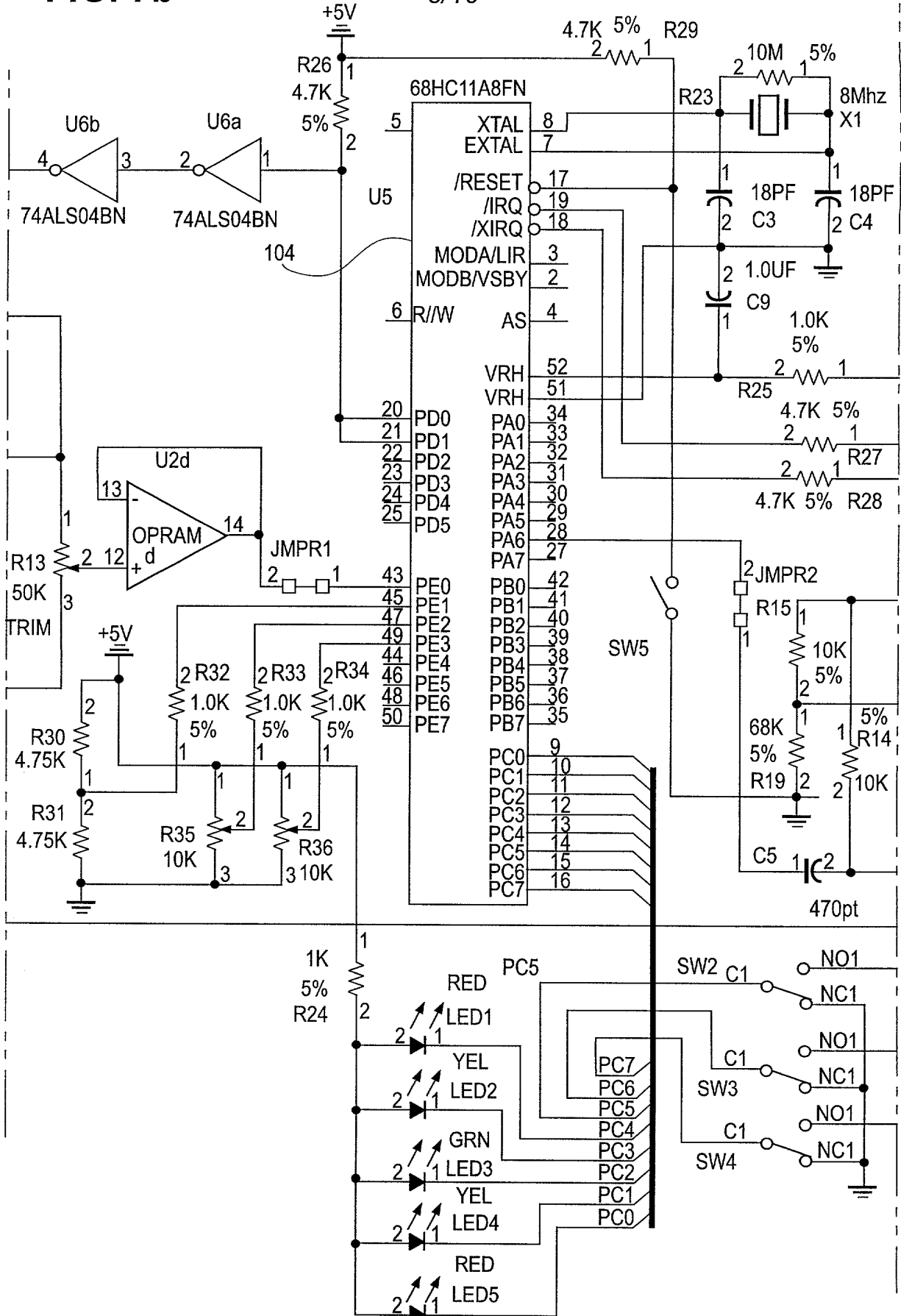


FIG. 7b

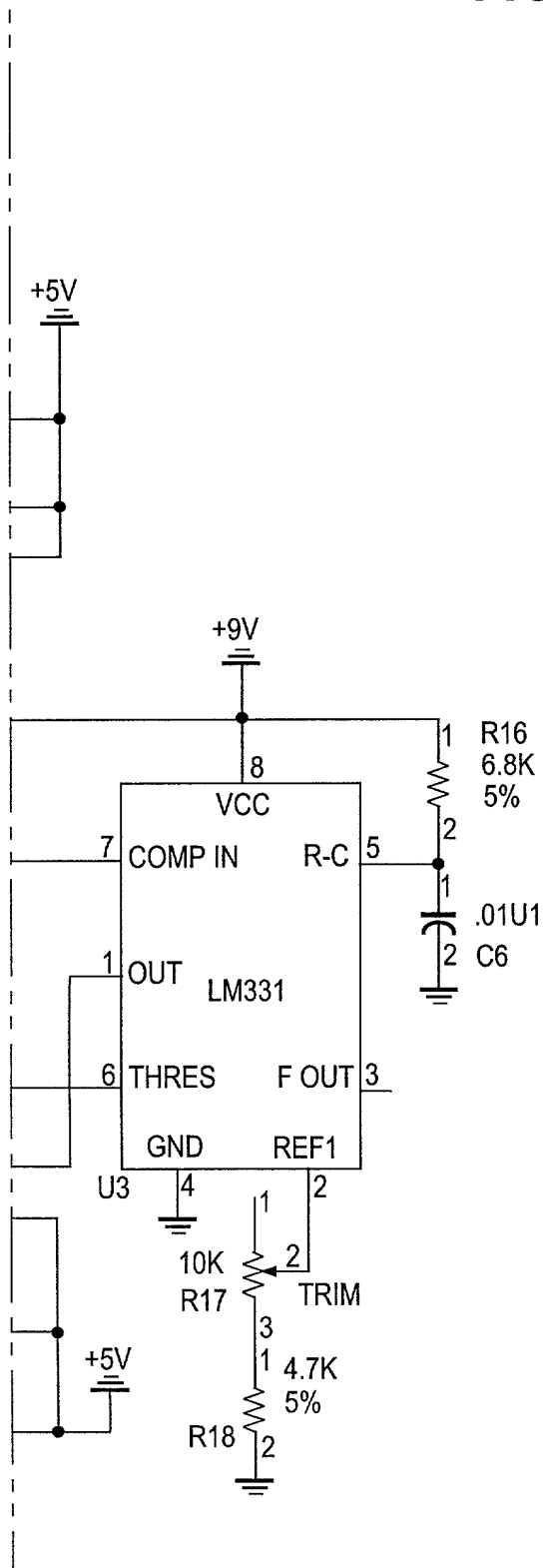
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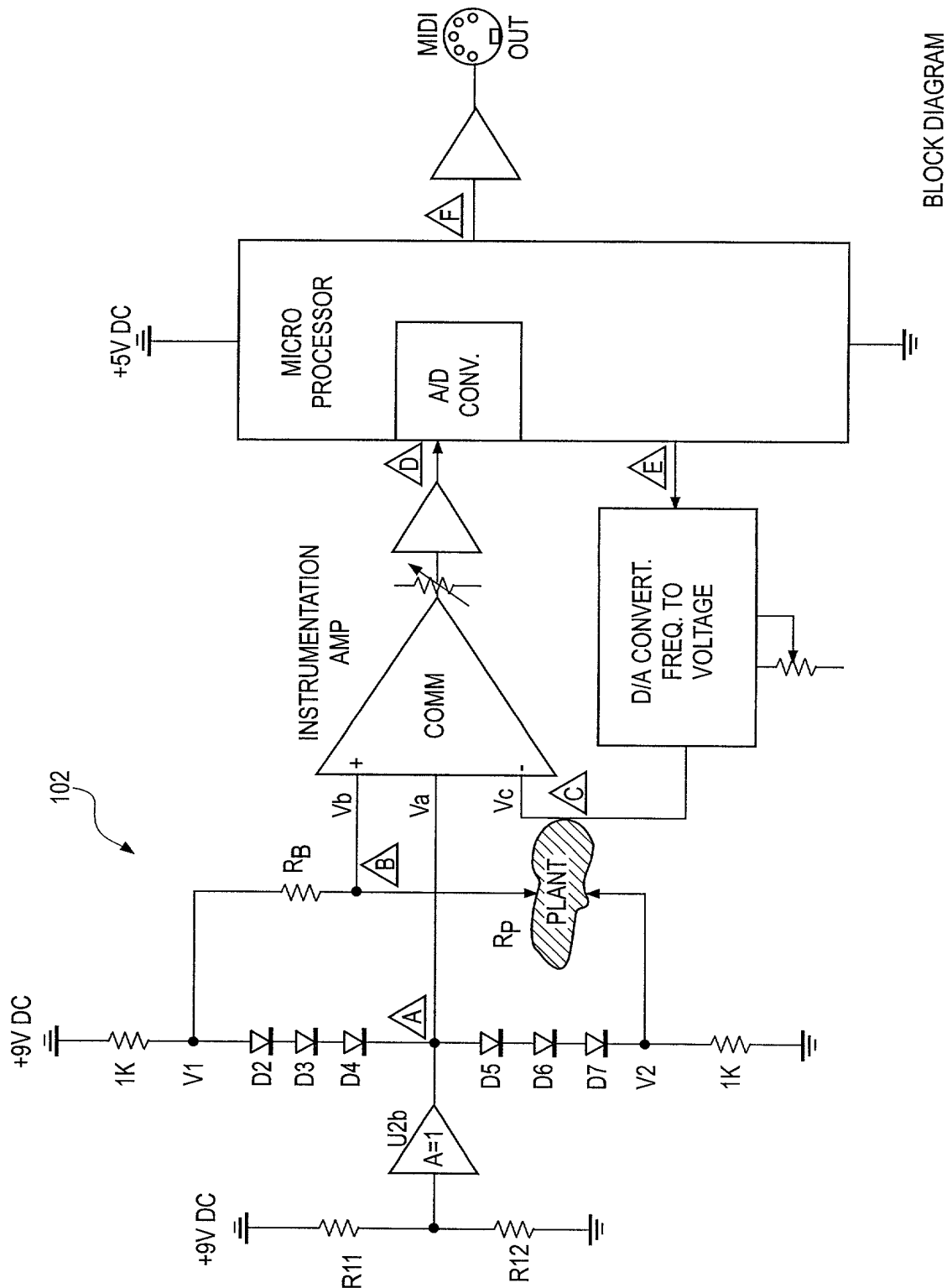
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FIG. 7c



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FIG. 8



BLOCK DIAGRAM